

Title: INTEGRATED CIRCUIT COMPRISING A VOLTAGE GENERATOR AND A CIRCUIT LIMITING THE VOLTAGE SUPPLIED BY THE VOLTAGE GENERATOR

Inventor(s): Jean Devin Express Mail No. EV336620485US Docket No. 852663.408

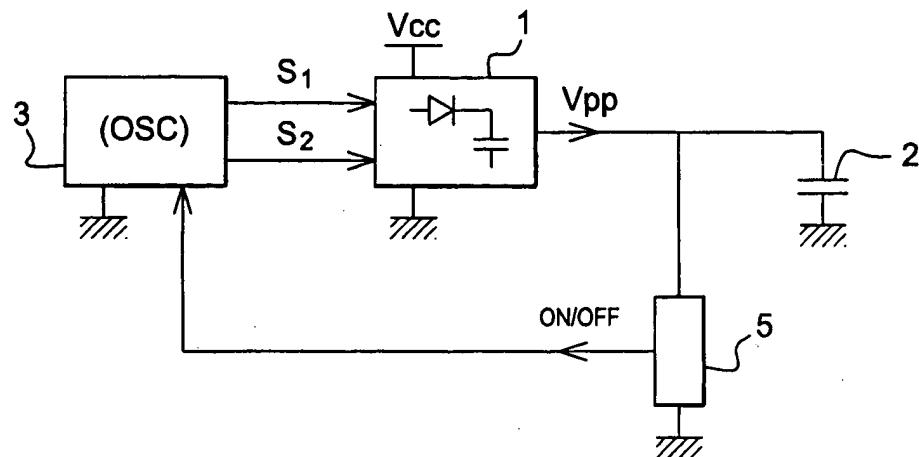


FIG. 1
(Prior Art)

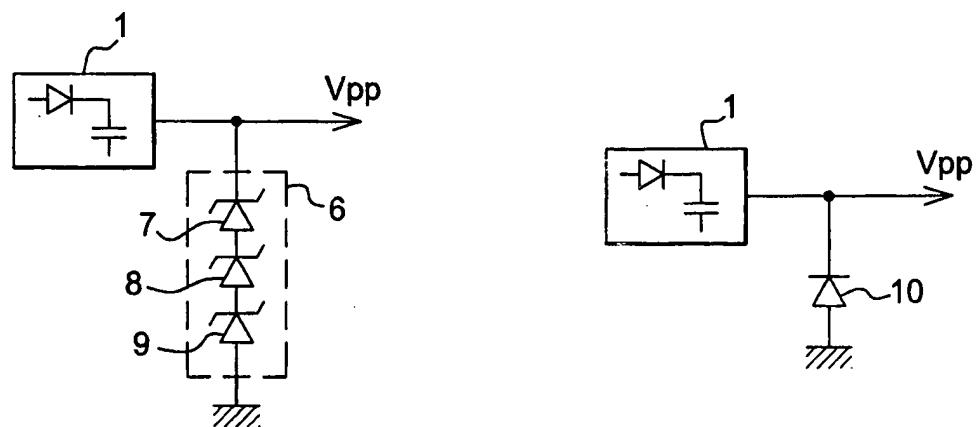


FIG. 2
(Prior Art)

FIG. 3
(Prior Art)

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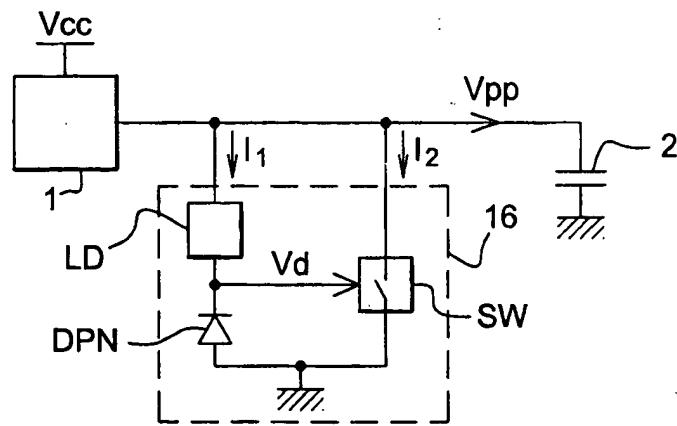


FIG. 4

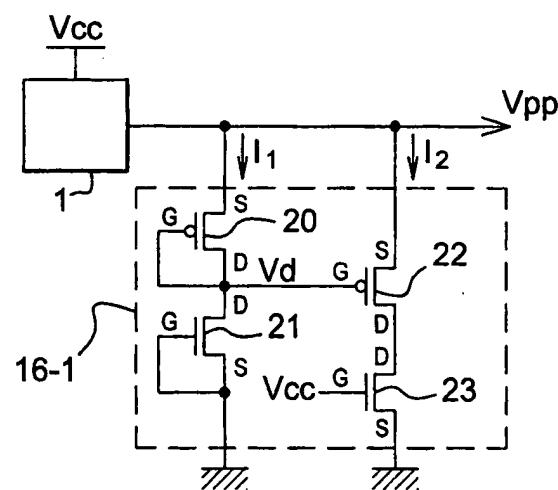


FIG. 5

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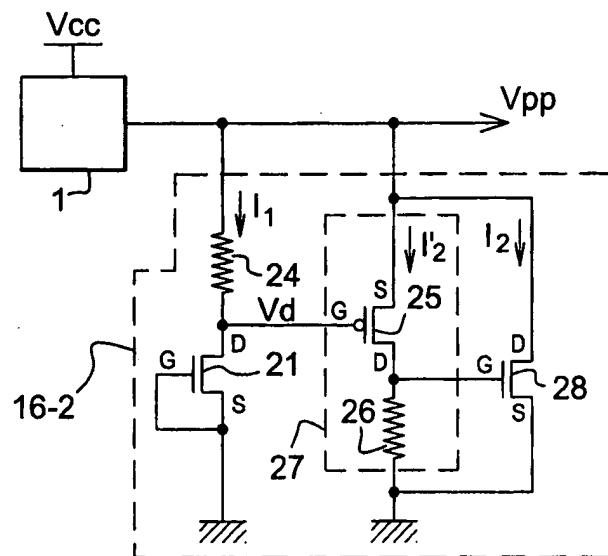


FIG. 6

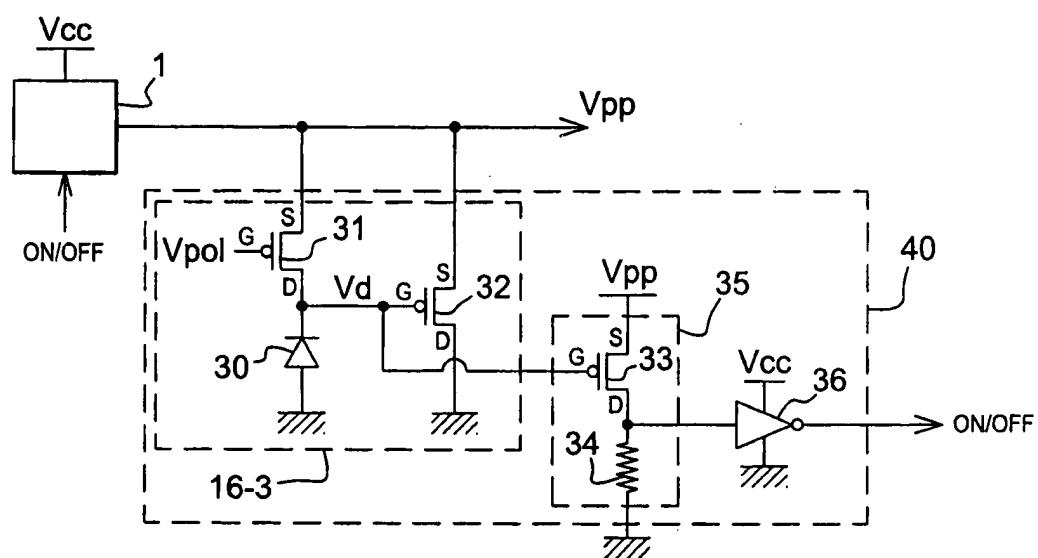


FIG. 7